



low density parity check error correction result

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[PDF] ► Decoding and equalization with analog non-linear networks

J Hagenauer, E Offer, C Measson, M Morz - To appear in European Transactions on ..., 1999 - Citeseer
 ... representations such as in **error** correcting coding. ... algorithm for his **low-density parity check** (LDPC) codes ... null' element under the addition (**XOR operation**). ...

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[PDF] ► Evaluating alternative implementations for LDPC decoder **check** node function

T Theoccharides, G Link, E Swankoski, N ... - IEEE Computer Society Annual Symposium on VLSI, 2004 - Citeseer

... **Low Density Parity Check** (LDPC) codes are a form of **error correction** ... communicate, is defined by the **parity check matrix** H , shown ... for each bit in a **check** (lhr 0 ...

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[PDF] ► Wyner-Ziv coding of video: an **error**-resilient compression framework

A Sehgal, A Jagmohan, N Ahuja - IEEE Transactions on Multimedia, 2004 - Citeseer
 ... and eliminates the propagation of **error** due to ... codes, in particular the

low-density-parity-check (LDPC) code ... 15] to generate the **parity-check matrix** , ...

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[PDF] ► Iterative decoding of **low-density parity check** codes

V Guruswami - Bull Eur Assoc Theor Comput Sci, 2006 - Citeseer

... In particular, we will study **low-density parity check** (LDPC) codes ... In other words, the **parity check matrix** of the code ... nodes have degree 3 and **check** nodes have ...

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[PDF] ► Design methodology for analog VLSI implementations of **error** control decoders

J Dai, 2002 - Citeseer

... codes such as Turbo codes, **low-density parity-check** codes, and ... low that a huge number of simulations must ... down the coefficients of the **parity-check** equation as ...

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[PDF] ► Nested codes with multiple interpretations

L Xiao, TE Fuja, J Kliwer, DJ Costello Jr - Proc. 40th Annual Conf. on Information Sciences and ..., 2006 - ece.nmsu.edu

... bits can be viewed as **parity** bits in either a **low density** generator **matrix** (LDGM) code or a lower-triangular **low density parity check** (LDPC) code [3 ...

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[PDF] ► Continuous network coding in wireless relay networks

W Pu, C Luo, S Li, CW Chen - INFOCOM 2008. The 27th Conference on Computer ..., 2008 - di.unito.it
 ... adaptivity, cooperative diversity, **low-density parity-check** codes, network ... and one side power **density** spectrum 0 N ... we generate additional **parity check** bits on ...

Cited by 3 - Related articles - View as HTML - All 2 versions

A multiple bit upset tolerant SRAM memory- ► 128.197.176.195 [PDF]

G Neuberger, F De Lima, L Carro, R Reis - ACM Transactions on Design Automation of ..., 2003 - portal.acm.org

... is area and timing efficient for high-**density** memories. ... in a single cycle, maintaining a **low** area overhead ... reduced area overhead, needs only 19 **parity** bits per ...

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Mixed-state entanglement and quantum error correction- ► arxiv.org (PDF)

CH Bennett, DP DiVincenzo, JA Smolin, WK ... - Physical Review A, 1996 - APS

... The right, **low**-order or ``amplitude" bit identifies the F ... from a Belldiagonal ensemble of known **density matrix** W, this ... I can be used to collect the **parity** of an ...

Cited by 2241 - Related articles - SI Direct - All 18 versions

[PDF] ► Faulttolerant linear finite state machines

CN Hadjicostis, GC Verghese - Proceedings of the 6th IEEE Int. Conf. on Electronics, ... , 1999 - Citeseer

... a failure in a single XOR gate (which results in a single-bit **error** in the state ...

To deal with this situation, we use **low** den- sity **parity check** (LDPC) codes ...

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